

15A, 700V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **15N70** is an N-Channel enhancement MOSFET, it uses UTC's advanced technology to provide customers with a minimum on-state resistance, high switching speed and low gate charge. It can also withstand high energy pulse in the avalanche and commutation modes.

The UTC **15N70** is suitable for high efficiency switching DC/DC converter, motor control and switch mode power supply.

■ FEATURES

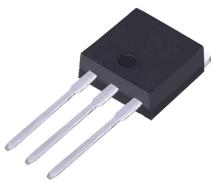
- * $R_{DS(ON)}=0.43\Omega$ @ $V_{GS}=10V, I_D=7.5A$
- * Low gate charge (Typ=70nC)
- * Low C_{RSS} (Typ=27pF)
- * High switching speed



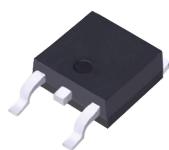
TO-220



TO-220F



TO-262



TO-263



TO-251



TO-252

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	$T_c=25^\circ\text{C}$	I_D	15
		$T_c=100^\circ\text{C}$		9.5
	Pulsed (Note 2)	I_{DM}	60	A
Avalanche Current (Note 2)		I_{AR}	15	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	950	mJ
	Repetitive (Note 2)	E_{AR}	30	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation ($T_c=25^\circ\text{C}$)		P_D	300	W
Derate above 25°C			2.38	W/ $^\circ\text{C}$
Junction Temperature		T_J	-55~+150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55~+150	$^\circ\text{C}$
Maximum Lead Temperature for Soldering Purposes, 1/8" from Case for 5 Seconds		T_L	300	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive rating; pulse width limited by max. junction temperature.
3. $L=7.8\text{mH}$, $I_{AS}=15\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$,
4. $I_{SD}\leq 15\text{A}$, $di/dt\leq 200\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, $T_J\leq 25^\circ\text{C}$.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	40	$^\circ\text{C/W}$
Junction to Case	θ_{JC}	0.42	$^\circ\text{C/W}$
Case to Sink	θ_{CS}	0.24	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	700			V
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Reference to 25°C , $I_D=250\mu\text{A}$		0.68		$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=700\text{V}, V_{GS}=0\text{V}$ $V_{DS}=560\text{V}, T_C=125^\circ\text{C}$		10	μA	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=+30\text{V}, V_{DS}=0\text{V}$ $V_{GS}=-30\text{V}, V_{DS}=0\text{V}$		+100	nA	
				-100	nA	
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	3.0		5.0	V
Static Drain-Source On-State Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=7.5\text{A}$		0.43	0.56	Ω
Forward Transconductance	g_{FS}	$V_{DS}=50\text{V}, I_D=7.5\text{A}$ (Note 1)		15		S
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1.0\text{MHz}$		2790	3600	pF
Output Capacitance	C_{OSS}			300	390	pF
Reverse Transfer Capacitance	C_{RSS}			27	35	pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{GS}=10\text{V}, V_{DS}=560\text{V}, I_D=15\text{A}$ (Note 1, 2)		70	90	nC
Gate to Source Charge	Q_{GS}			17		nC
Gate to Drain Charge	Q_{GD}			33		nC
Turn-ON Delay Time	$t_{D(\text{ON})}$	$V_{DD}=350\text{V}, I_D=15\text{A}, R_G=25\Omega$ (Note 1, 2)		70	150	ns
Rise Time	t_R			180	370	ns
Turn-OFF Delay Time	$t_{D(\text{OFF})}$			160	330	ns
Fall-Time	t_F			120	250	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				15	A
Maximum Body-Diode Pulsed Current	I_{SM}				60	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=15\text{A}, V_{GS}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time	t_{RR}	$I_S=15\text{A}, V_{GS}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		460		ns
Body Diode Reverse Recovery Charge	Q_{RR}	(Note 1)		5.7		μC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature