

4.0A, 800V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **4N80** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers planar stripe and DMOS technology. This technology is specialized in allowing a minimum on-state resistance, and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

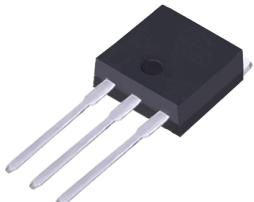
The UTC **4N80** is universally applied in high efficiency switch mode power supply.



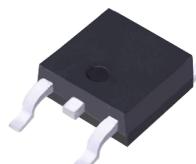
TO-220



TO-220F



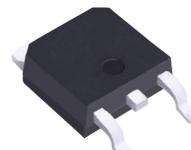
TO-262



TO-263



TO-251



TO-252

4N80

Power MOSFET

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	4.0	A
	Pulsed (Note 2)	I_{DM}	16	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	460	mJ
	Repetitive (Note 2)	E_{AR}	13	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.0	V/ns
Power Dissipation	TO-220/TO-262	P_D	106	W
	TO-220F/TO-220F1		36	W
	TO-220F2		38	W
	TO-251/TO-251L		50	W
	TO-252/TO-252D			
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L=57\text{mH}$, $I_{AS}=4\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$

4. $I_{SD} \leq 4\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F1/TO-220F2			
Junction to Case	TO-262	θ_{JC}	110	$^\circ\text{C/W}$
	TO-251/TO-251L			
	TO-252/TO-252D			
	TO-220/TO-262	θ_{JC}	1.18	$^\circ\text{C/W}$
	TO-220F/TO-220F1		3.47	$^\circ\text{C/W}$
	TO-220F2		3.28	$^\circ\text{C/W}$
	TO-251/TO-251L		2.5	$^\circ\text{C/W}$
	TO-252/TO-252D			

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■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	800			V
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}}=250\mu\text{A}$, Referenced to 25°C		950		mV°C
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=800\text{V}, V_{\text{GS}}=0\text{V}$		10		μA
		$V_{\text{DS}}=640\text{V}, T_C=125^\circ\text{C}$		100		μA
Gate-Source Leakage Current	Forward	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=30\text{V}$		100		nA
	Reverse	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-30\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	3.0		5.0	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=2\text{A}$		2.3	3.0	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}, f=1.0\text{MHz}$		680	880	pF
Output Capacitance	C_{OSS}			75	100	pF
Reverse Transfer Capacitance	C_{RSS}			8.6	12	pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{\text{DS}}=640\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}$ (Note 1,2)		19	25	nC
Gate-Source Charge	Q_{GS}			4.2		nC
Gate-Drain Charge	Q_{GD}			9.1		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D(ON)}}$	$V_{\text{DD}}=400\text{V}, I_{\text{D}}=4\text{A}, R_{\text{G}}=25\Omega$ (Note 1,2)		16	40	ns
Turn-ON Rise Time	t_{R}			45	100	ns
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			35	80	ns
Turn-OFF Fall Time	t_{F}			35	80	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				4	A
Maximum Body-Diode Pulsed Current	I_{SM}				16	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=4\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$V_{\text{GS}}=0\text{V}, I_S=4\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$ (Note 1)		575		ns
Body Diode Reverse Recovery Charge	Q_{rr}			3.65		μC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

- TYPICAL CHARACTERISTICS

