

12N50

Power MOSFET

12A, 500V N-CHANNEL POWER MOSFET

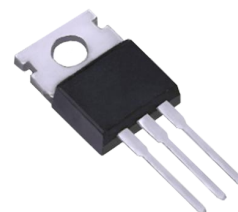
■ DESCRIPTION

The UTC **12N50** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **12N50** is generally applied in high efficiency switch mode power supplies, active power factor correction and electronic lamp ballasts based on half bridge topology.

■ FEATURES

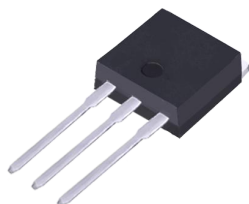
- * $R_{DS(ON)} < 0.54\Omega @ V_{GS}=10V$
- * High Switching Speed
- * 100% Avalanche Tested



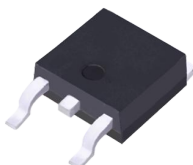
TO-220



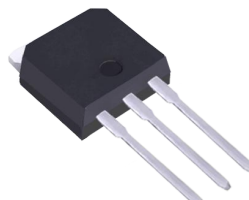
TO-220F



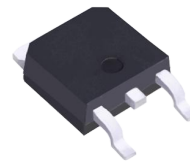
TO-262



TO-263



TO-251



TO-252

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■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous ($T_C=25^\circ\text{C}$)	I_D	12 (Note 2)	A
	Pulsed (Note 3)	I_{DM}	48 (Note 2)	A
Avalanche Current (Note 3)		I_{AR}	12	A
Avalanche Energy	Single Pulsed (Note 4)	E_{AS}	684	mJ
	Repetitive (Note 5)	E_{AR}	19.5	mJ
Peak Diode Recovery dv/dt (Note 5)		dv/dt	4.5	V/ns
Power Dissipation ($T_C=25^\circ\text{C}$)	TO-220/ TO-263	P_D	195	W
	TO-220F/TO-220F1		42	
	TO-220F2		43	
Derate above 25°C	TO-220/TO-263		1.53	W/ $^\circ\text{C}$
	TO-220F/TO-220F1		0.33	
	TO-220F2		0.34	
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55~+150	$^\circ\text{C}$

- Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Drain current limited by maximum junction temperature
 3. Repetitive Rating: Pulse width limited by maximum junction temperature
 4. $L = 9.5\text{mH}$, $I_{AS} = 12\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
 5. $I_{SD} \leq 12\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220/ TO-263	θ_{JC}	0.65	$^\circ\text{C}/\text{W}$
	TO-220F/TO-220F1		3.0	
	TO-220F2		2.9	

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■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	500			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V			10	μA
Gate- Source Leakage Current	Forward	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse	V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =6A		0.42	0.54	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		1450	1930	pF
Output Capacitance	C _{OSS}			198	265	pF
Reverse Transfer Capacitance	C _{RSS}			14.5	22	pF
SWITCHING PARAMETERS						
Total Gate Charge	Q _G	V _{GS} =10V, V _{DS} =400V, I _D =12A (Note 1, 2)		30	39	nC
Gate to Source Charge	Q _{GS}			8		nC
Gate to Drain Charge	Q _{GD}			12		nC
Turn-ON Delay Time	t _{D(ON)}	V _{DD} =250V, I _D =12A, R _G =25Ω (Note 1, 2)		28	65	ns
Rise Time	t _R			54	120	ns
Turn-OFF Delay Time	t _{D(OFF)}			75	160	ns
Fall-Time	t _F			47	105	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				12	A
Maximum Body-Diode Pulsed Current	I _{SM}				48	A
Drain-Source Diode Forward Voltage	V _{SD}	I _S =12A, V _{GS} =0V			1.5	V
Body Diode Reverse Recovery Time	t _{rr}	I _S =12A, V _{GS} =0V, dI _F /dt=100A/μs		154		ns
Body Diode Reverse Recovery Charge	Q _{RR}	(Note 1)		0.45		μC

- Note: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%
 2. Essentially independent of operating temperature

■ TYPICAL CHARACTERISTICS

