

5N90

Power MOSFET

5A, 900V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **5N90** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specialized in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **5N90** is universally applied in high efficiency switch mode power supply.

■ FEATURES

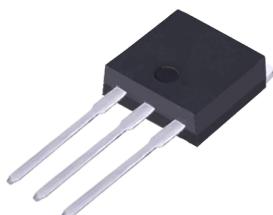
- * $R_{DS(ON)} < 2.8\Omega$ @ $V_{GS}=10V$, $I_D=2.5A$
- * High switching speed
- * Improved dv/dt capability
- * 100% avalanche tested



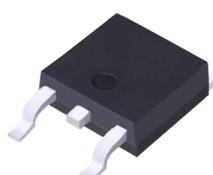
TO-220



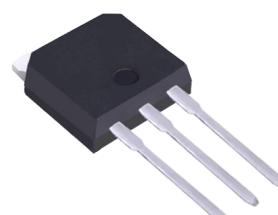
TO-220F



TO-262



TO-263



TO-251



TO-252

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■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	900	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	5	A
	Pulsed (Note 2)	I_{DM}	12	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	350	mJ
	Repetitive (Note 2)	E_{AR}	5.1	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.0	V/ns
Power Dissipation	TO-220/TO-262/TO-263	P_D	125	W
	TO-220F/TO-220F1		47	
	TO-3P		240	
Junction Temperature	T_J		+150	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55~+150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L=28\text{mH}$, $I_{AS}=5\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$

4. $I_{SD} \leq 5.4\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F1/TO-262			
	TO-263		40	
Junction to Case	TO-220/TO-262	θ_{JC}	1	$^\circ\text{C/W}$
	TO-263		3.66	
	TO-220F/TO-220F1		0.52	
	TO-3P			

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ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	900			V
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}}=250\mu\text{A}$, Referenced to 25°C		1.0		$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=900\text{V}, V_{\text{GS}}=0\text{V}$		10		μA
		$V_{\text{DS}}=720\text{V}, T_C=125^\circ\text{C}$		100		μA
Gate-Source Leakage Current	Forward	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=30\text{V}$		100		nA
	Reverse	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-30\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	3.0		5.0	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=2.5\text{A}$		2.0	2.8	Ω
Forward Transconductance	g_{FS}	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=2.5\text{A}$ (Note 1)		4.0		S
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$ $I_G=3.3\text{mA}$		1200	1550	pF
Output Capacitance	C_{OSS}			110	145	pF
Reverse Transfer Capacitance	C_{RSS}			13	17	pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{\text{DS}}=120\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=5\text{A}$ (Note 1,2)		140	160	nC
Gate-Source Charge	Q_{GS}			12		nC
Gate-Drain Charge	Q_{GD}			30		nC
Turn-ON Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=1\text{A}, R_G=25\Omega$ (Note 1,2)		70	90	ns
Turn-ON Rise Time	t_R			106	140	ns
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			196	220	ns
Turn-OFF Fall Time	t_F			110	130	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				5	A
Maximum Body-Diode Pulsed Current	I_{SM}				12	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S = 5\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$V_{\text{GS}}=0\text{V}, I_S=5.4\text{A},$		610		ns
Body Diode Reverse Recovery Charge	Q_{RR}	$dI_{\text{F}}/dt=100\text{A}/\mu\text{s}$ (Note 1)		5.26		μC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

- TYPICAL CHARACTERISTICS

