

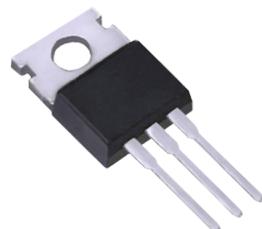
9N90

Power MOSFET

9A, 900V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **9N90** uses UTC's advanced proprietary, planar stripe, DMOS technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

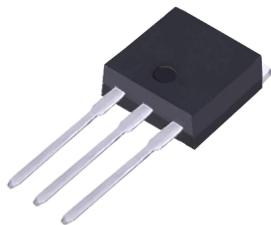


TO-220

- * $R_{DS(ON)} \leq 1.2\Omega$ @ $V_{GS}=10V$, $I_D=4.5A$
- * Ultra Low Gate Charge (Typical 45 nC)
- * Low Reverse Transfer Capacitance ($CRSS =$ Typical 14 pF)
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness



TO-220F



TO-262



TO-263



TO-251



TO-252

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Power MOSFET

■ ABSOLUTE MAXIMUM RATING ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	900	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	9.0	A
Pulsed Drain Current (Note 2)	I_{DM}	36	A
Avalanche Current (Note 2)	I_{AR}	9.0	A
Avalanche Energy	Single Pulsed(Note 3)	E_{AS}	900 mJ
	Repetitive(Note 2)	E_{AR}	28 mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.0 V/ns	
Power Dissipation	TO-247	P_D	160 W
	TO-3P/TO-3PB		240
	TO-3PN		56
	TO-220F1		58
	TO-220F2		
Linear Derating Factor above $T_C = 25^\circ\text{C}$	TO-247		1.28 W/°C
	TO-3P/TO-3PB		1.92
	TO-3PN		0.448
	TO-220F1		0.464
	TO-220F2		
Junction Temperature	T_J	+150	°C
Storage Temperature	T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

3. $L = 21\text{mH}$, $I_{AS} = 9.0\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 9.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-247	θ_{JA}	50 °C/W
	TO-3P/TO-3PB		40
	TO-3PN		62.5
Junction to Case	TO-247	θ_{JC}	0.78 °C/W
	TO-3P/TO-3PB		0.52
	TO-3PN		2.25
	TO-220F1		
	TO-220F2		2.15

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ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	900			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=900\text{V}, V_{\text{GS}}=0\text{V}$		10		μA
Gate-Body Leakage Current	Forward	I_{GSSF}	$V_{\text{GS}}=30\text{V}, V_{\text{DS}}=0\text{V}$		100	nA
	Reverse	I_{GSSR}	$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$		-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	3.0		5.0	V
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=4.5\text{A}$			1.2	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		1870		pF
Output Capacitance	C_{OSS}			185		pF
Reverse Transfer Capacitance	C_{RSS}			21		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge(Note 1)	Q_G	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=1.3\text{A}, I_{\text{G}}=100\mu\text{A}$ (Note1,2)		215		nC
Gate-Source Charge	Q_{GS}			17		nC
Gate-Drain Charge	Q_{GD}			44		nC
Turn-On Delay Time(Note 1)	$t_{\text{D(ON)}}$	$V_{\text{DD}}=30\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=0.5\text{A}, R_{\text{G}}=25\Omega$ (Note1,2)		100		ns
Turn-On Rise Time	t_R			170		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			410		ns
Turn-Off Fall Time	t_F			175		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				9.0	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				36	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=9.0\text{A}$			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=9.0\text{A}, \frac{dI_F}{dt}=100\text{A}/\mu\text{s}$ (Note 1)		550		ns
Reverse Recovery Charge	Q_{rr}			6.5		μC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TYPICAL CHARACTERISTICS

